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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

U.S. Serial No. 10/679.727

APPLICANT: Li, et al.

TITLE: DIGITAL CAMERA INTERFACE

FILED: October 6, 2003

ART UNIT: 2612

ATTORNEY DOCKET: DP-310006

EXAMINER: Unknown

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on:

March 10, 2005
Date

Kandace Brown
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**STATEMENT ACCOMPANYING
INFORMATION DISCLOSURE STATEMENT**

Applicant(s) requests the Examiner to consider and make of record the reference(s) and/or information disclosed herein or on the attached PTO 1449.

CHECK ONE: (A, B, or C.)

☒ A. This statement is submitted within 1) three months after the filing date (even if after the first action); or 2) within three months of the date of entry of the national stage or 3) before the mailing date of a first Office Action on the merits. No fee or statement is required.

☐ B. This statement is submitted after the period specified in para. A, but before Final Office Action or Notice of Allowance or the close of prosecution.

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☐ 1. I hereby state that each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement; or

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☐ 2. I hereby state that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of the information disclosure statement.

☐ 3. Charge the \$180 fee set forth in 37 CFR §1.17(p) to Delphi Technologies, Inc. Deposit Account No. 50-0831.

☐ C. This statement is submitted after a Final Office Action or Notice of Allowance or the close of prosecution, but before payment of the issue fee. Charge the \$180 fee set forth in 37 CFR §1.17(p) to Delphi Technologies, Inc. Deposit Account No. 50-0831.

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☐ 2. I hereby state that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of the information disclosure statement.

If any additional fee or any additional amount of fee be required in connection with this Information Disclosure Statement, Applicant respectfully requests that such fee or amount of fee be charged to Delphi Technologies, Inc. Deposit Account No. 50-0831.

Respectfully submitted,



Stefan V. Chmielewski – 39,941
Delphi Technologies, Inc.
Telephone (248) 813-1220

enc: PTO 1449 and references

INFORMATION DISCLOSURE CITATION WITH DOCUMENT COPIES



Atty. Docket No. DP-310006	Serial No. 10/679,727
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Applicant Li, et al.

Filing Date October 6, 2003	Group 2612
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U.S. PATENT DOCUMENTS

Exam. Init.	Document Number	Date	Name	Class	Sub Class	Filing Date (if approp.)
	6,515,271	04/04/2003	Shimizu			
	6,549,239	04/15/2003	Tao			
	2003/0103141	06/05/2003	Bechtel, et al.			

FOREIGN PATENT DOCUMENTS

Document Number	Date	Country	Class	Subclass	Translation Yes No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

| Texas Instruments: "TSB12LV31 IEEE 1394-1995 General-Purpose Link Layer Controller", September 1998. XP002167311.

| Texas Instruments: "TSB11LVO1 3-V 1-Port IEEE 1394-1995 CABLE TRANSCEIVER/ARBITER", May 1997, XP002314718.

| "DS92LVO90A 9 Channel Bus LVDS Transceiver", February 2001, National Semiconductor, XP002314719.

| "AviiVA M2 CL", April 2003, Atmel XP002314720.

| "Camera Link - Specifications of the Camera Link Interface Standard for Digital Cameras and Frame Grabbers", October 2000, XP002314721.

| "DS92LV1021A 16-40 MHz 10 Bit Bus LVDS Serializer", January 2003, National Semiconductor, XP002314722.

| "DS92LV1212A 16-40 MHz 10-Bit Bus LVDS Random Lock Deserializer with Embedded Clock Recovery", November 2000, National Semiconductor, XP002314723.

| "DS90LV019 3.3V or 5V LVDS Driver/Receiver", August 2000, National Semiconductor, XP002314724.

|

 Examiner

 |Date Considered

***Examiner: Initial if reference considered whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.**
